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### **Research Article**

# MODELING OF BOOST AND CUK CONVERTERS AND COMPARISON OF THEIR PERFORMANCE IN MPPT

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### ABSTRACT

The efficiency of photovoltaic (PV) panel systems depends on the structure of the photovoltaic cells and the transfer of energy from the PV panel to load. The focus of this study is energy transfer. The most important component in the energy transfer is maximum power point tracking (MPPT). The MPPT consists of two components: the algorithm for calculation the maximum power and the hardware for power generating. This study investigates how some the DC-DC converters (hardware) impact on the performance of the MPPT under six different environmental conditions by using two different duty cycle calculation methods. Contribution of this study to the literatüre is to analyze the performance of some the DC-DC converters in MPPT by completely eliminating the effect of MPPT algorithms. As a result of this study, while using the Boost converter in MPPT applications, the internal resistance of the PV panel must be equal or smaller than load resistance. However, the Cuk converter can reach to maximum power point in all conditions such as temperature, radiation and load.

**Keywords:** Circuit linearization, DC-DC converter modelling, state-space averaging, maximum power point tracking (MPPT).

### 1. INTRODUCTION

Demand for renewable energy as global is increasing rapidly because of population and economic growth. Solar energy is the leading source of the renewable energies. Photovoltaic cells are used to convert this energy into electrical energy. The efficiency of commercially available crystalline silicon-based solar cells is between 15% and 22%. In recent years, an efficiency of 46% with compound based multijunction solar cells has been reached in laboratory conditions [1]. However, this rate is still very low. Therefore, the energy that can convert to electrical energy should be used without the losses. To reduce the losses, the photovoltaic panels must be operated at the maximum power point [2,3].

Generating maximum power from the PV panels is possible by equalizing the converter input equivalent resistance to the PV panel internal resistance [4]. If a random load is connected to the PV panel, a certain amount of power can be generated. In addition, if a load equalled to the internal resistance of the PV panel is connected at certain radiation and temperature, it can also

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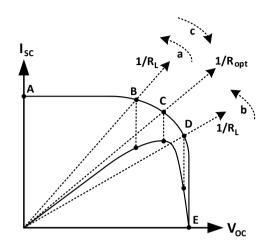
generate the ma ximum power. However, in case of a change in environmental conditions (radiation, temperature, etc.), it is impossible to generate the maximum power from the PV panel. For this reason, the converters are used to eliminate this state [4]. The duty cycles of the converters are set by using the MPPT algorithm.

In the literature, the MPPT studies have been carried out by using many different converters [5-9]. In these studies, radiation or temperature as a variable have been selected and algorithmic performance analyses have been performed [8,9]. In addition, the selected load values are random. In another study, performance of six different converters (Buck, Boost, Buck-Boost, Cuk, Sepic, Zeta) in MPPT is compared [10]. However, no mathematical model was developed through these studies. In the study, incremental conductance algorithm was used as the MPPT algorithm. In another study, three different converters (Buck, Boost, Buck-Boost) are investigated [11]. Perturb and observe algorithm was used in the study as the MPPT algorithm. In another comprehensive study in the literature, five different algorithms (Perturb and Observe, Incremental Conductance, Fractional Open Circuit Voltage, Ripple Correlation Control, Extremum Seeking Control) and three different converters (Buck, Boost, Buck-Boost) are investigated [12]. Another study that used three different DC-DC converters was carried out by using the neural-network (NN) controller and perturb & observe (P&O) algorithm [4]. However, performance of converters in tracking of maximum power point (MPP) in these studies [4,8 - 12] were not analyzed by completely eliminating the effect of MPPT algorithms. In this study, unlike other studies, it only analyzes the performance of some converters in tracking MPP by completely eliminating the effect of MPPT algorithms.

In the analysis of the converters, six different environmental conditions and two different duty cycle calculation method have been applied. In the first method, the duty cycle is calculated by using the proportional-integral-derivative (PID) controller. The method is completely mathematical. For this reason, the converters are modelled to design the PID controller. Reference for the PID controller is directly obtained from catalog. In the second method, the duty cycle is calculated based on the Perturb and Observe (PNO) algorithm. The PNO algorithm can directly calculate the duty cycle and reach to the MPP without modelling the converters. According to the PNO algorithm, the MPP is point where the derivative of the power with respect to voltage is zero. The PNO algorithm by using a fixed step size changes the duty cycle to reach MPP. The other MPPT algorithms can also reach to the MPP by tracking the power change as mentioned in the PNO algorithm [7,13]. The information about the MPPT algorithms is limited to the information given in this study because there are many studies on the MPPT algorithms in literature [13-16].

## 2. CRITERIA TO BE CONSIDERED IN THE SELECTION OF CONVERTERS IN MPPT APPLICATIONS

In order to generate maximum power from the PV panels, the internal resistance of the PV panel must be equal to the converter input equivalent resistance. For this purpose, the converter input equivalent resistance is equalized to the internal resistance of the PV panel by observing the duty cycle. The internal resistance of the PV panel ( $R_{opt}$ ) is constant in a given radiation and temperature. According to Figure 1, the ability of Boost and Cuk converters to reach MPP at different load values is investigated.



**Figure 1.** PV panel short circuit current  $(I_{SC}) x$  open circuit voltage  $(V_{OC})$ 

For the Boost converter, the relationship between internal resistance of the PV panel ( $R_{opt}$ ) and the converter input equivalent resistance ( $R_L$ ) is defined as follows [17]:

$$R_{ont} = R_L (1-D)^2 \tag{1}$$

If the duty cycle is equal to zero, the converter input equivalent conductivity  $(1/R_L)$  given in Figure 1 is at 'A' point. If the converter input equivalent resistance is smaller than the internal resistance of the PV panel and the duty cycle is equal to zero, the slope of the line representing the converter input equivalent conductivity shown in Figure 1 is higher than the slope of the line representing the PV panel internal conductivity. For this reason, the control of duty cycle can be enabled between 'A' and 'B' points in 'a' and 'c' direction. However, since the MPP is the 'C' point, the Boost converter can not reach to the MPP. If the converter input equivalent resistance is greater than the internal resistance of the PV panel and the duty cycle is equal to zero, the slope of the line representing the converter input equivalent conductivity. In this case, the control of duty cycle can be enabled between 'A' and 'D' points (including 'C' point) in 'a' and 'c' direction. Because the MPP is 'C' point, the Boost converter can reach to the MPP.

For the Cuk converter, the relationship between the internal resistance of the PV panel and the converter input equivalent resistance is defined as follows [18]:

$$R_{opt} = R_L \left(\frac{1-D}{D}\right)^2 \tag{2}$$

If the duty cycle is equal to zero, the converter input equivalent conductivity is at the 'E' point. If the duty cycle is equal to zero, the converter input equivalent conductivity is at the 'A' point. Since 'C' point is between 'A' and 'E' points, the Cuk converter can reach to the MPP in all loads. The losses of inductor, capacitor, diode and switching components that are used in this study are neglect. For this reason, these components are used as ideal for this study in next chapters.

### 3. MATHEMATICAL MODEL OF BOOST CONVERTER

The Boost converter consists of inductor, capacitor, diode and switching components. Output voltage of the Boost converter is greater than input voltage and output current of the Boost converter is smaller than input current. The block diagram of the Boost converter is shown in Figure 2.

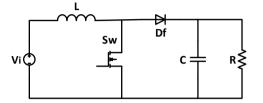


Figure 2. The block diagram of the Boost converter

The parameter values of the Boost converter used in this study are given in Table 1.

Table 1. Parameter values of the Boost converter

Parameter	Value
L	2mH
С	250µF
Switching frequency	50 kHz

The switching states of the Boost converter are shown as below. Switch on:

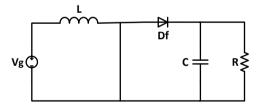


Figure 1. The Boost converter in switch on

$$L\frac{di_{L}}{dt} = V_{g}$$
(3)

$$C\frac{dV_{c}}{dt} = -\frac{1}{R}V_{c} - i_{Z}$$
<sup>(4)</sup>

Equation (5) is obtained by writing the above equations (3, 4) in vector matrix form  $\dot{(x = A_1 x + B_1 u)}$ .

$$\begin{bmatrix} \mathbf{i}\\ \mathbf{i}_{L}\\ \mathbf{v}_{C} \end{bmatrix} = \begin{bmatrix} 0 & 0\\ 0 & -1/RC \end{bmatrix} \begin{bmatrix} \mathbf{i}_{L}\\ \mathbf{V}_{C} \end{bmatrix} + \begin{bmatrix} 1/L\\ 0 \end{bmatrix} \begin{bmatrix} V_{g} \end{bmatrix} + \begin{bmatrix} 0\\ -1/C \end{bmatrix} \begin{bmatrix} \mathbf{i}_{Z} \end{bmatrix}$$
(5)

Switch off:

- -

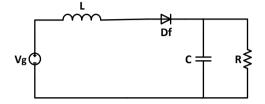


Figure 2. The Boost converter in switch off

$$L\frac{di_{L}}{dt} = V_{g}$$
(6)

$$C\frac{dV_{c}}{dt} = -\frac{1}{R}V_{c} + i_{L} - i_{Z}$$
<sup>(7)</sup>

Equation (8) is obtained by writing the above equations (6, 7) in vector matrix form.

$$\begin{bmatrix} \mathbf{i}_{L} \\ \mathbf{i}_{L} \\ \mathbf{v}_{C} \end{bmatrix} = \begin{bmatrix} 0 & -1/L \\ 1/C & -1/RC \end{bmatrix} \begin{bmatrix} i_{L} \\ V_{C} \end{bmatrix} + \begin{bmatrix} 1/L \\ 0 \end{bmatrix} \begin{bmatrix} V_{g} \end{bmatrix} + \begin{bmatrix} 0 \\ -1/C \end{bmatrix} \begin{bmatrix} i_{Z} \end{bmatrix}$$
(8)

For the switch on and switch off situations of the Boost converter, the state equations in given equation (5, 8) are combined to obtain the average large signal model.

$$A = A_{1}d + A_{2}(1-d)$$

$$B = B_{1}d + B_{2}(1-d)$$

$$C = C_{1}d + C_{2}(1-d)$$

$$D = D_{1}d + D_{2}(1-d)$$
(9)

$$\begin{bmatrix} \mathbf{i}\\ \mathbf{i}_{L}\\ \mathbf{v}_{C} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1-d}{L}\\ \frac{1-d}{C} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} \mathbf{i}_{L}\\ \mathbf{v}_{C} \end{bmatrix} + \begin{bmatrix} \frac{1}{L} & 0\\ 0 & -\frac{1}{C} \end{bmatrix} \begin{bmatrix} \mathbf{v}_{g}\\ \mathbf{i}_{Z} \end{bmatrix}$$
(10)

Obtained the average large signal model is composed of steady state (D) and small signal  $(\hat{d})$  model. For the steady state situation, the average large signal model 'd' is changed into 'D', and all other variables are represented by an upper case letter.

$$\mathbf{v}_{c} = \mathbf{V}_{c} + \mathbf{v}_{c} \tag{11}$$

$$\dot{\mathbf{i}}_{\mathrm{L}} = \mathbf{I}_{\mathrm{L}} + \dot{\tilde{\mathbf{i}}}_{\mathrm{L}} \tag{12}$$

$$\dot{\mathbf{i}}_{z} = \mathbf{I}_{z} + \dot{\tilde{\mathbf{i}}}_{z} \tag{13}$$

$$\mathbf{d} = \mathbf{D} + \hat{\mathbf{d}} \tag{14}$$

$$\mathbf{v}_{g} = \mathbf{V}_{g} + \stackrel{\wedge}{\mathbf{v}}_{g} \tag{15}$$

In the large signal model, the small signs and steady state signs in given equations (11-15) are put in equation (10) and the equation (16) is obtained.

$$\begin{bmatrix} \cdot \\ I_L + i_L \\ \cdot \\ V_C + v_C \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1-D+d}{L} \\ \frac{1-D+d}{L} \\ \frac{1-D+d}{C} & \frac{1}{RC} \end{bmatrix} \begin{bmatrix} I_L + i_L \\ V_C + v_C \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} \begin{bmatrix} V_g + v_g \\ V_g \end{bmatrix} + \begin{bmatrix} 0 \\ -\frac{1}{C} \end{bmatrix} \begin{bmatrix} I_Z + i_Z \end{bmatrix}$$
(16)

State variables and input variables in the average large signal model are composed of two components which consist of the average values in the operating point and the oscillation values around it.

To obtain a small signal model from the equation (16),

- 1- AX + BU = 0
- 2- The multiplication of two small signal are supposed that it are zero [19].

$$\begin{bmatrix} \cdot & \cdot \\ I_{L} + i_{L} \\ \cdot & \cdot \\ V_{C} + v_{C} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{(1-D)}{L} \\ \frac{1-D}{C} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} I_{L} \\ V_{C} \end{bmatrix} + \begin{bmatrix} \frac{1}{L} \\ -\frac{1}{C} \end{bmatrix} \begin{bmatrix} V_{g} \\ I_{Z} \end{bmatrix}$$

$$+ \begin{bmatrix} 0 & \frac{\hat{d}}{L} \\ \frac{\hat{d}}{L} & 0 \end{bmatrix} \begin{bmatrix} \cdot \\ i_{L} \\ v_{C} \end{bmatrix} + \begin{bmatrix} 0 & -\frac{\hat{d}}{L} \\ \frac{\hat{d}}{C} & 0 \end{bmatrix} \begin{bmatrix} I_{L} \\ V_{C} \end{bmatrix} + \begin{bmatrix} 0 & -\frac{(1-D)}{L} \\ V_{C} \end{bmatrix} \begin{bmatrix} \cdot \\ i_{L} \\ v_{C} \end{bmatrix} + \begin{bmatrix} 0 & -\frac{(1-D)}{L} \\ 0 \end{bmatrix} \begin{bmatrix} \cdot \\ i_{L} \\ v_{C} \end{bmatrix} + \begin{bmatrix} 0 \\ -\frac{1}{C} \end{bmatrix} \begin{bmatrix} \cdot \\ i_{Z} \end{bmatrix}$$
(17)

The equation (17) is rearranged below by taking into account the assumptions and equation (18) is obtained.

$$\begin{bmatrix} \vdots \\ i_L \\ \vdots \\ v_C \end{bmatrix} = \begin{bmatrix} 0 & -\frac{(1-D)}{L} \\ \frac{1-D}{C} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} A \\ i_L \\ v_C \end{bmatrix} + \begin{bmatrix} 1 & 0 & \frac{V_C}{L} \\ L & -\frac{1}{C} & -\frac{i_L}{C} \end{bmatrix} \begin{bmatrix} A \\ v_g \\ A \\ i_Z \\ A \\ d \end{bmatrix}$$
(18)

State equation  $(\hat{x} = A\hat{x} + B\hat{u})$  is obtained. There are three input signs: Duty cycle  $(\hat{d})$ , PV panel output voltage  $(\hat{v}_g)$  and load current  $(\hat{i}_z)$ . In the controller design, the PV panel output voltage and load current changes are taken as zero.

Output equation:

$$\hat{y} = C \hat{x} + D \hat{u}$$
<sup>(19)</sup>

Since the input has directly no effect on the output, D = 0.

$$\begin{bmatrix} \uparrow \\ v_0 \\ i_i \end{bmatrix} = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} \uparrow \\ i_L \\ \uparrow \\ v_C \end{bmatrix}$$
(20)

The transfer function of the Boost converter is obtained by using the coefficients matrices of the state equations. The PID controller design is based on the transfer function,

$$\hat{\frac{i}{i}}_{a} = C[sI - A]^{-1}[B] = \frac{1.477e04s + 1.182e07}{s^{2} + 400s + 693842}$$
(21)

Since PID coefficients design methods are outside the scope of this study, the PID coefficients are obtained by using Matlab interface designs.

Kp = 0.0105, Ki = 16.2, Kd = 1.69e-06 (22)

### 4. MATHEMATICAL MODEL OF CUK CONVERTER

The inductor, capacitor and resistance are linear circuit elements. These components are modeled by using linear differential equations. But diode and switching components are non-linear. Therefore, linearization has to be done for control. The block diagram of the Cuk converter is shown in the Figure 5.

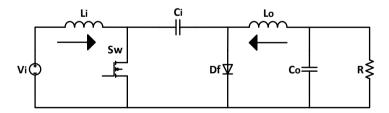


Figure 5. The block diagram of the Cuk converter

The parameter values of the Cuk converter used in this study are given in Table 2.

Parameter	Value	
Li	5mH	
Lo	5mH	
Ci	47µF	
Co	1µF	
Switching frequency	50 kHz	

Table 2. Parameter values of the Cuk converter

In the first step for linearization, Cuk converter differential equations are obtained according to two different switching states. Cuk converter state space equations are written in vector matrix form using steady state average value approach [20]. The small signal model of the Cuk converter is used for the operating point determined in the controler design. This model obtained is given below according to the switching status.

Switch on:

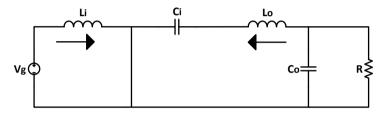


Figure 6. The Cuk converter in switch on

$$L_{i}\frac{di_{Li}}{dt} = V_{g}$$
<sup>(23)</sup>

$$L_0 \frac{di_{L0}}{dt} = -V_{Ci} - V_{C0}$$
(24)

$$C_{i} \frac{dV_{Ci}}{dt} = i_{L0}$$
<sup>(25)</sup>

$$C_0 \frac{dV_{C0}}{dt} = i_{L0} - \frac{V_{C0}}{R}$$
(26)

Equation (27) is obtained by writing the equations in the form of vector matrix.

$$\begin{bmatrix} \mathbf{i} \\ i_{Li} \\ \mathbf{i} \\ \mathbf{v}_{Ci} \\ \mathbf{v}_{Ci} \\ \mathbf{v}_{C0} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & 0 & -1/L_0 & 0 \\ 0 & 1/C_i & 0 & 0 \\ 0 & 1/C_0 & 0 & -1/RC_0 \end{bmatrix} \begin{bmatrix} i_{Li} \\ i_{L0} \\ \mathbf{v}_{Ci} \\ \mathbf{v}_{C0} \end{bmatrix} + \begin{bmatrix} 1/L_i \\ 0 \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} v_g \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \\ 0 \\ -1/C_0 \end{bmatrix} \begin{bmatrix} i_z \end{bmatrix}$$
(27)

Switch off:

F

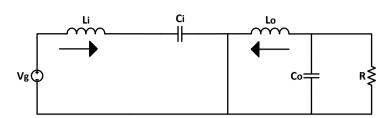


Figure 3. The Cuk converter in switch off

$$L_{i} \frac{di_{Li}}{dt} = V_{g} - V_{ci}$$
<sup>(28)</sup>

$$L_0 \frac{di_{L0}}{dt} = -V_{C0} \tag{29}$$

$$C_{i} \frac{dV_{Ci}}{dt} = i_{Li}$$
(30)

$$C_0 \frac{dV_{C0}}{dt} = i_{L0} - \frac{V_{C0}}{R}$$
(31)

Equation (32) is obtained by writing the equations in the form of vector matrix.

$$\begin{bmatrix} \mathbf{i}_{L_{i}} \\ \mathbf{i}_{L_{0}} \\ \mathbf{v}_{C_{i}} \\ \mathbf{v}_{C_{i}} \\ \mathbf{v}_{C_{0}} \end{bmatrix} = \begin{bmatrix} 0 & 0 & -1/L_{i} & 0 \\ 0 & 0 & 0 & -1/L_{0} \\ 1/C_{i} & 0 & 0 & 0 \\ 0 & 1/C_{0} & 0 & -1/RC_{0} \end{bmatrix} \begin{bmatrix} i_{L_{i}} \\ i_{L_{0}} \\ \mathbf{v}_{C_{i}} \\ \mathbf{v}_{C_{0}} \end{bmatrix} + \begin{bmatrix} 1/L_{i} \\ 0 \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} v_{g} \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \\ 0 \\ -1/C_{0} \end{bmatrix} \begin{bmatrix} i_{z} \end{bmatrix}$$
(32)

The average large signal model is obtained by combining the state equations in given equation (27, 32) for the Cuk converter's switch on and switch off situations.

$A = A_1 d + A_2 (1 - d)$	
$B = B_1 d + B_2 (1 - d)$	(33)
$C = C_1 d + C_2 (1 - d)$	
$D = D_1 d + D_2 (1 - d)$	

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$$\begin{bmatrix} i_{Li} \\ i_{L0} \\ \cdot \\ v_{Ci} \\ \cdot \\ v_{Co} \end{bmatrix} = \begin{bmatrix} 0 & 0 & -(\frac{1-d}{L_i}) & 0 \\ 0 & 0 & -\frac{d}{L_0} & \frac{-d+d-1}{L_0} \\ \frac{1-d}{C_i} & \frac{d}{Ci} & 0 & 0 \\ 0 & \frac{d+1-d}{C_0} & 0 & \frac{-d+d-1}{RC_0} \end{bmatrix} \begin{bmatrix} i_{Li} \\ i_{L0} \\ v_{Ci} \\ v_{Co} \end{bmatrix} + \begin{bmatrix} \frac{d}{L_i} \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} v_s \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} v_s \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \\ 0 \\ -d/C_0 \end{bmatrix} \begin{bmatrix} i_z \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \\ 0 \\ d-1/C_0 \end{bmatrix} \begin{bmatrix} i_z \end{bmatrix}$$
(34)

Obtained the average large signal model is composed of steady state (D) and small signal  $(\hat{d})$  model. For the steady state situation, the average large signal model 'd' is changed into 'D', and all other variables are represented by an upper case letter.

$$d = D + \hat{d} \tag{35}$$

$$v_{C0} = V_{C0} + \dot{v}_{C0} \tag{36}$$

$$v_{Ci} = V_{Ci} + \hat{v}_{ci}$$
 (37)

$$v_g = V_g + v_g^{\wedge} \tag{38}$$

$$\dot{i}_{Z} = I_{Z} + \dot{\tilde{i}}_{Z} \tag{39}$$

$$\dot{i}_{Li} = I_{Li} + \dot{\tilde{i}}_{Li} \tag{40}$$

$$\dot{i}_{L0} = I_{L0} + \dot{\tilde{i}}_{L0} \tag{41}$$

In the large signal model, the small signs and steady state signs in given equations (35-41) are put in equation (34) and the equation (42) is obtained.

State variables in the large signal model  $(i_{L1}, i_{L2}, v_{C1}, v_{C0})$  and input variables  $(v_g, i_Z, d)$  consists of two components which consist of the average value in the operation point and the oscillation around it.

To obtain a small signal model from equation (42),

- 1- AX + BU = 0
- 2- It is supposed that the multiplication of two small signals is zero [19].

$$\begin{bmatrix} \dot{i}_{ll} \\ \dot{i}_{ll} \\ \dot{i}_{l0} \\ \dot{k}_{l0} \\ \dot{$$

(43)

The equation (43) is rearranged below by taking into account the assumptions and equation (44) is obtained.

$$\begin{bmatrix} \cdot \\ i_{L_{l}} \\ \cdot \\ i_{L_{0}} \\ \cdot \\ v_{C_{0}} \\ \cdot \\ v_{C_{0}} \end{bmatrix} = \begin{bmatrix} 0 & 0 & \frac{D-1}{L_{i}} & 0 \\ 0 & 0 & -\frac{D}{L_{0}} & -\frac{1}{L_{0}} \\ \frac{1-D}{C_{i}} & \frac{D}{C_{i}} & 0 & 0 \\ 0 & \frac{1}{C_{0}} & 0 & -\frac{1}{RC_{0}} \end{bmatrix} \begin{bmatrix} i_{L_{l}} \\ i_{L_{0}} \\ v_{C_{0}} \\ \cdot \\ v_{C_{0}} \end{bmatrix} + \begin{bmatrix} \frac{V_{C_{l}}}{L_{i}} \\ -\frac{V_{C_{l}}}{L_{0}} \\ \frac{-I_{L_{l}}+I_{L_{0}}}{C_{i}} \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} i \\ v_{g} \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} i \\ v_{g} \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} i \\ -\frac{1}{RC_{0}} \end{bmatrix} \begin{bmatrix} i \\ z \end{bmatrix}$$
(44)

State equation  $(\hat{x} = A\hat{x} + B\hat{u})$  is obtained. There are three input signs: duty cycle  $(\hat{d})$ , panel output voltage  $\begin{pmatrix} & \\ v_{\sigma} \end{pmatrix}$  and load current  $\begin{pmatrix} & \\ i_{7} \end{pmatrix}$ . In the controller design, the panel output voltage and load current changes are taken to zero.

Output equation:

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$$\hat{y} = C x + D u$$
(45)

Since the input has no effect on the output, D = 0.

$$\begin{bmatrix} \hat{v}_{0} \\ \hat{i}_{i} \\ \hat{i}_{i} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{i}_{Li} \\ \hat{i}_{L0} \\ \hat{v}_{Ci} \\ \hat{v}_{Co} \end{bmatrix}} + \underbrace{\begin{bmatrix} 0 & 0 \\ 0 \\ D_{u} \end{bmatrix}}_{D_{u}}$$
(46)

The transfer function of the Cuk converter is obtained by using the coefficients matrices of the state equations. The PID controller design is based on the transfer function.

$$\frac{\hat{l}_i}{d} = C[sI - A]^{-1}[B] = \frac{9200s^3 + 9.232e08s^2 + 2.185e12s + 3.103e15}{s^4 + 1e05s^3 + 2.023e08s^2 + 2.269e11s + 1.171e14}$$
(47)

Since PID coefficients design methods are out of the scope of this study, PID coefficients were obtained by using Matlab interface designs.

$$Kp = 0.074, Ki = 39.9, Kd = 4.19e - 06$$
 (48)

### 5. SIMULATION RESULTS

In the previous sections, the mathematical model of Cuk and Boost converters was obtained and the PID controllers were designed. In this section, performances of Boost and Cuk converters are analyzed under different conditions such as radiation, temperature and load. The duty cycle is calculated in two different ways for the operation of converters. In the first, the current at MPP  $(I_{MPP})$  value of the PV panel under the specified conditions is taken from the catalog. This  $I_{MPP}$ 

value is used as reference ( $I_{REF}$ ) for PID controller. The current of PV panel ( $I_{PV}$ ) is measured and the duty cycle of converters is calculated by PID controller. In the second, controller are not designed and duty cycle is calculated by the PNO algorithm that based on the measurement of the PV panel current ( $I_{PV}$ ) and voltage ( $V_{PV}$ ). The step size of the PNO algorithm is 0.01. The algorithm performs a measurement of every 0.02s.

The first method is completely deterministic, but it is not applicable in real applications because it is very difficult to find an  $I_{MPP}$  value by measuring continuous environmental conditions. Therefore, this method is used exclusively for this study. The second method contains no deterministic operations and it focuses only on monitoring the system.

The block diagram of the system generated in the Matlab / Simulink program is given in Figure 8.

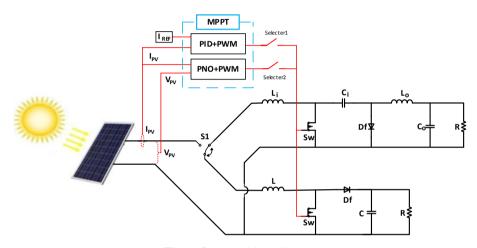


Figure 8. MPPT block diagram

The signal as given reference current is the current value at the maximum point of the PV panel. Electrical characteristics of the PV panel that is used in this study are shown in Table 3[21].

 Table 3. The PV panel characteristics in STC (STC refers to temperature of 25°C and irradiation of 1000 W/m².)

Electrical performance of KC85T	Value
Power at MPP (Pmax)	87.35 W
Voltage at MPP (Vmpp)	17.4 V
Current at MPP (Impp)	5.02 A
Open Circuit Voltage (Voc)	21.7 V
Short Circuit Current (Isc)	5.35 A

When the environmental conditions change, the internal resistance of the PV panel changes. Radiation-panel internal resistance and temperature-panel internal resistance curves of KC85T panel are presented in Figure 9 and Figure 10, respectively.

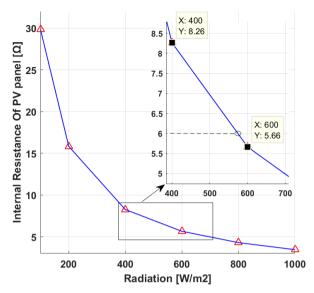


Figure 9. Internal resistance of PV panel-radiation

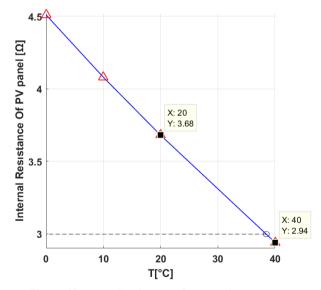
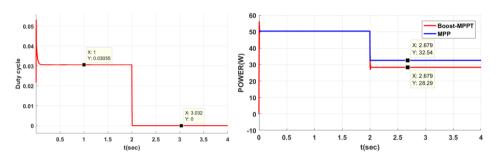


Figure 10. Internal resistance of PV panel-temperature

The important factors about the choice of converter in PV systems were mentioned in the second chapter. These factors are studied at the cases 1, 2, 3 and 4.



**Case 1:** Radiation: 600 W/m<sup>2</sup> (0s – 2s) - 400 W/m<sup>2</sup> (2s – 4s), Load: 6 ohms, Temperature: 25°C Converter: Boost converter

Figure 11. Calculation of duty cycle by using PID Figure 12. Output power of PV when using PID

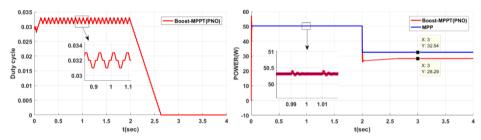


Figure 13. Calculation of duty cycle by using PNO Figure 14. Output power of PV when using PNO

In case 1, radiation is variable while temperature and load are constant. In the range of 0-2s, the internal resistance of the PV panel (5.66 ohms) is less than the Boost converter input equivalent resistance (6 ohms). For this reason, the Boost converter can reach to the MPP by using both the PID controller and the PNO algorithm. In the range of 2-4s, the internal resistance of the PV panel (8.26 ohms) is greater than the Boost converter input equivalent resistance (6 ohms). In this period of time, the Boost converter can not reach to the MPP by using both the PID controller and the PNO algorithm. The duty cycle calculated using the PID controller and PNO algorithm is shown in Figure 11 and Figure 13, respectively.

The Boost converter can only transfer of 28.29 W to the load as shown in Figure 12 and Figure 14. But, the MPP of PV panel is 32.54 W. So the efficiency is 86%.

**Case 2:** Radiation: 1000 W/m<sup>2</sup>, Load: 3 ohms, Temperature:  $40^{\circ}C (0s - 2s) - 20^{\circ}C (2s - 4s)$ Converter: Boost converter

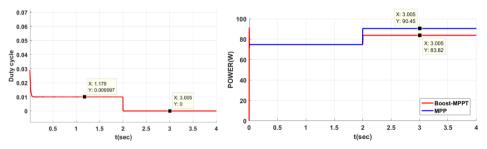


Figure 15. Calculation of duty cycle by using PID Figure 16. Output power of PV when using PID

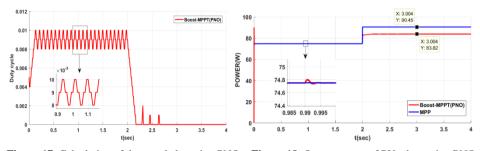


Figure 17. Calculation of duty cycle by using PNO Figure 18. Output power of PV when using PNO

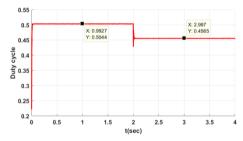
In case 2, temperature is variable while radiation and load are constant. In the range of 0-2s, the internal resistance of the PV panel (2.94 ohms) is less than the Boost converter input equivalent resistance (3 ohms). For this reason, the Boost converter can reach to the MPP by using both the PID controller and the PNO algorithm. In the range of 2-4s, due to decreasing temperature, the internal resistance of the PV panel rise to 3.68 ohms. So the internal resistance of the PV panel is greater than the Boost converter input equivalent resistance (3 ohms). In this period of time, the Boost converter can not reach to the MPP by using both the PID controller and the PNO algorithm. The duty cycle calculated using the PID controller and PNO algorithm is shown in Figure 15 and Figure 17, respectively.

The Boost converter can only transfer of 83.82 W to the load as shown in Figure 16 and Figure 18. But, the MPP of PV panel is 90.45 W. There is of 6.63 W loss. The power loss in both case 1 and case 2 is only due to the converter.

**Case 3:** Radiation: 600 W/m<sup>2</sup> (0s - 2s) - 400 W/m<sup>2</sup> (2s - 4s), Load: 6 ohms, Temperature: 25°C Converter: Cuk converter

60

POWER(W)



Cuk-MPPT 50 40 52 30 51 34 50 20 49 32 10 48 0.8 0.9 2.9 0 0 0.5 1.5 2 2.5 3.5 3 t(sec)

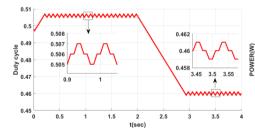


Figure 19. Calculation of duty cycle by using PID



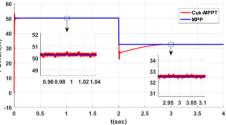
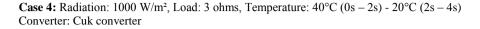


Figure 21. Calculation of duty cycle by using PNO

Figure 22. Output power of PV when using PNO



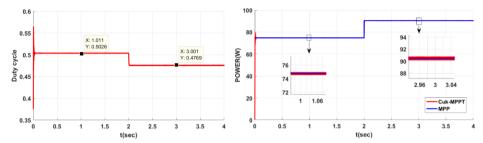


Figure 23. Calculation of duty cycle by using PID

Figure 24. Output power of PV when using PID

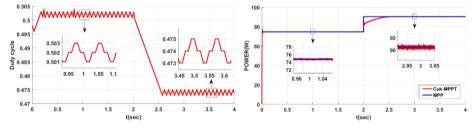


Figure 25. Calculation of duty cycle by using PNO

Figure 26. Output power of PV when using PNO

In case 3 and case 4, the Cuk converter tracks MPP without affected from environmental conditions such as radiation, temperature and variable load while calculating duty cycle by using both PID and PNO methods. Although case 1 and case 2 is are same as case 3 and case 4 in terms of environmental conditions, the output power of the PV panel is different because of converters. In case 1, 2, 3 and 4, performance of the used converters in MPPT is shown in Table 4.

Temperature	Radiation	PV panel internal resistance	Load	Cuk MPPT Performance	Boost MPPT Performance	
	400W/m <sup>2</sup>	8.26 Ω	- 6Ω -	~	X	
25C°	400 ₩/Ш			<b>v</b>	×	
250	600W/m <sup>2</sup>	5.66 Ω	5 66 0	- 012	✓	~
	000 w/m			<b>v</b>	~	
40C°	2.94 Ω · ·	2.94 Ω	1000W/m <sup>2</sup>		~	X
40C					- 3Ω -	~
20C°	- 1000 W/III-	3.68 Ω			512	~
200				~	<b>v</b>	

### 6. CONCLUSION

In this study, the MPP tracking performances of Boost and Cuk converters are investigated. The system is simulated on MATLAB/Simulink platform. While investigating these performances, it should be determined that environmental conditions and duty cycle calculation methods are very important. Therefore, six different environmental conditions and two different duty cycle calculation methods are used. In the calculation of duty cycle of the converters, the PID based method which is completely mathematical and the PNO algorithm based on observation are used. The both duty cycle calculation methods have same response under the different environmental conditions. The performance of the converters in MPPT is clearly shown in Table 4. According to Table 4, the Cuk converter can reach to the MPP in each case whereas the Boost converter can reach to the MPP only at greater the load resistance than the PV panel internal resistance.

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